

# Power Quality Improvement using Unified Power Quality Conditioner with Distributed Generation

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**Abstract**— Recently power quality has become more important issue. Now a day's power electronics based appliances are widely used in industries and in distribution system which creates more power quality problems. The power electronics based power conditioning devices can be an effective solution to improve power quality in power system. Unified Power Quality Conditioner (UPQC) is one of the custom power devices which are used to solve voltage and current related problems simultaneously. In this paper, combined operation of UPQC with Distributed Generation (DG) is discussed. This system integrated with wind energy is able to compensate voltage sag/swell, load current disturbances. Also proposed system is able to compensate voltage interruption and active power transfer to load and source in both interconnected and islanding mode and help to improve power quality. The operation of UPQC with DG has been evaluated through simulation studies using MATLAB/SIMULINK software.

**Index Terms**— Uninterruptible Power Supplies (UPS), Unified Power Quality Conditioner (UPQC), Distributed Generation (DG), Point of Common Coupling (PCC), Voltage Source Inverter (VSI), Distribution Static Compensator (DSTATCOM), Dynamic Voltage Restorer (DVR), Fast Fourier Transform (FFT).

## 1 INTRODUCTION

IN electrical power system power electronics devices play an important role. In distribution system it has three aspects first one is that introduces valuable industrial and domestic equipments, second one is that creates problems, third one is that help to solve problems. Now a day's modern semiconductor switching devices such as controlled rectifiers, Uninterruptible Power Supplies (UPS), arc furnaces etc. are widely used particularly in domestic and industrial loads.

These non linear loads create power quality problems such as voltage sag, voltage swell, voltage interruption, voltage flickers, voltage spikes, harmonics etc. Such poor power quality causes increase in power losses and other remarkable abnormalities in distribution sides. Thus, it is very important to maintain a high standard of power quality. Earlier passive filters were used to solve power quality problems. However because of some limitations of passive filters, now a day's custom power devices are used to solve power quality problems in distribution side.

The compensating custom power devices are used for active filtering, load balancing, power factor improvement and voltage regulating (sag/swell). There are three types of custom power devices: Distribution Static Compensator (DSTATCOM), Dynamic Voltage Restorer (DVR) and Unified Power Quality Conditioner (UPQC).

Unified Power Quality Conditioner (UPQC) is one of the custom power devices, which can solve voltage and current related problem simultaneously. This is connected before load to make load voltage distortion free and at the same time reactive current drawn from source should be compensated in such a way that the currents at source side would be in phase with supply voltage.

The interest in Distributed Generation (DG) has been increased rapidly. The world wide concern about environmental pollution and the energy shortage has led to the increasing interest in generation of renewable electrical energy. As Dis-

tribution Generation (DG) play very important role in power system and help to solve many problems that ac conventional power system has. There are several DGs such as PV system, fuel cell, wind turbine. Wind power has become fastest growing energy source among various renewable energy source. In this paper deals with combined operation of UPQC with wind energy and output of DG system is connected to DC bus of UPQC. The UPQC with DG help to compensate Voltage and current power quality problems and have give additional benefit by providing the power to load whenever voltage interruption occur with source side [1].

This paper discussed combined operation of UPQC with DG and this system is integrated with wind energy. The proposed system is able to compensate voltage sag/swell, load current disturbances. In addition to this it is able to compensate voltage interruption and active power transfer to load and source in both interconnected and islanding mode and help to improve power quality. The operation of UPQC with DG has been evaluated through simulation studies using MATLAB/SIMULINK software [2].

## 2 SYSTEM DESCRIPTION

UPQC has two voltage-source inverters which are connected back to back by common DC bus. A series inverter is connected through transformer between source and PCC and a shunt inverter is connected across load. Series inverter is responsible for mitigation of supply side disturbances such as voltage sag/swell, flickers, voltage unbalance. It inserts voltage so as to maintain the load voltage at desire level, balanced and distortion free. The shunt inverter is responsible for mitigating the current related problems caused by consumers such as poor power factor, load harmonic currents, load unbalance etc. It injects current in system in such a way that source current become balanced, sinusoids and in phase with the supply

voltage. The general block diagram of UPQC is shown in figure 1.

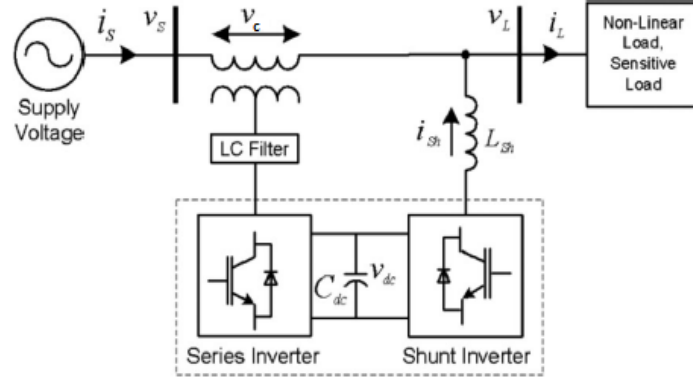


fig1. General block diagram of UPQC

In this paper, the system under consideration is as shown in figure 2. It consists of three phase four wire UPQC with wind energy source as DG and its output is connected to DC bus of UPQC. The system neutral is connected to the negative terminal of DC link voltage to avoid the requirement of fourth leg in Voltage Source Inverter (VSI) of shunt active filter [3],[4]. This system has two modes of operation - interconnected mode in which DG provide power to source and load and islanding mode in which DG provide power to load within its power rating.

The proposed system also consists of two DC storage device but each leg of VSI can be controlled independently.  $V_{sa}, V_{sb}, V_{sc}$  are three phase source voltages  $V_{ta}, V_{tb}, V_{tc}$  are the terminal voltages and voltages injected by series active filters  $V_{inja}, V_{inj b}$  and  $V_{inj c}$  of phase a, b and c respectively. The three phase source currents are  $i_{sa}, i_{sb}, i_{sc}$ . The load currents are  $i_{la}, i_{lb}, i_{lc}$  and current injected by shunt active filter are  $i_{fa}, i_{fb}, i_{fc}$ . The feeder resistance and inductance are  $R_s$  and  $L_s$  and respectively. The interfacing inductance and resistance of shunt active filter are  $L_f$  and  $R_f$  respectively. The interfacing inductance and capacitance of series active filter  $L_{se}$  are  $C_{se}$  and respectively. The total DC link voltage is  $V_{dc bus} (V_{dc1} + V_{dc2}) = 2V_{dc}$  and  $I_n$  is the neutral current.

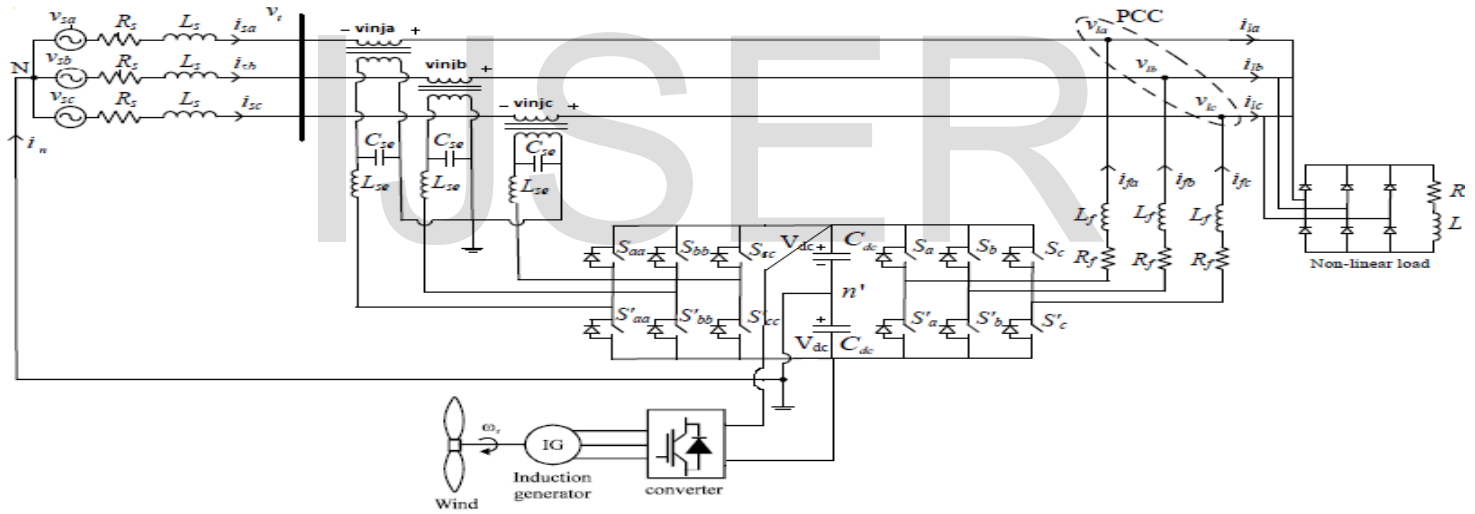


fig2. Proposed system

### 3 CONTROLLER DESIGN

#### 3.1 Extraction of Unit Vector Template

Input voltage source contains fundamental and other distorted component at PCC. The UPQC should separate out fundamental positive sequence components from other components. The unit vector template can be obtained by multiplying input voltage with gain which is equal to  $1/V_m$ , where  $V_m$  is nothing but the peak amplitude of fundamental input voltage. For synchronization of signals these unit vector templates are passed through a Phase Lock Loop (PLL) system and finally we get the unit vector templates for different phases [5].

$$U_a = \sin(\omega t)$$

$$U_b = \sin(\omega t - 120)$$

$$U_c = \sin(\omega t - 240)$$

#### 3.2 Generation of Reference Compensator Currents

The terminal voltage becomes unbalanced and distorted whenever unbalanced and distorted load current flowing through the feeder impedance. The series active filters makes the voltages at PCC balanced and sinusoidal but it still contains switching frequency components and they contain some distortions. If this terminal voltage used for generating shunt

filter current reference then it results in erroneous compensation. Thus, fundamental positive sequence voltages  $v_{1a1}^+(t)$ ,  $v_{1b1}^+(t)$ , and  $v_{1c1}^+(t)$  are extracted from PCC and used to generate reference current for shunt active filter. The equation for reference compensator current is given in equation (1). In this equation  $P_{lavg}$  is average load power and is obtained using a moving average filter of one cycle window of time  $T$  in seconds.  $P_{loss}$  denotes the switching losses and ohmic losses and generated using a capacitor voltage PI controller. The term is desired phase angle between the source voltage and current and for unity power factor,  $\gamma$  is set to zero [3], [4], [6].

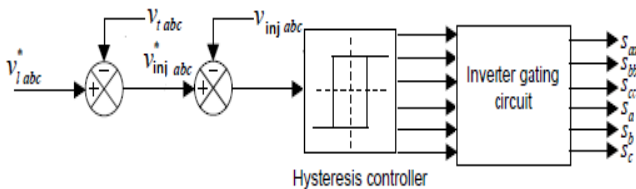
$$\begin{aligned}
 i_{fa}^* &= i_{la} - i_{sa}^* = i_{la} - \frac{v_{1a1}^+ + \gamma(v_{1b1}^+ - v_{1c1}^+)}{\Delta_1^+} (P_{lavg} + P_{loss}) \\
 i_{fb}^* &= i_{lb} - i_{sb}^* = i_{lb} - \frac{v_{1b1}^+ + \gamma(v_{1c1}^+ - v_{1a1}^+)}{\Delta_1^+} (P_{lavg} + P_{loss}) \\
 i_{fc}^* &= i_{lc} - i_{sc}^* = i_{lc} - \frac{v_{1c1}^+ + \gamma(v_{1a1}^+ - v_{1b1}^+)}{\Delta_1^+} (P_{lavg} + P_{loss})
 \end{aligned}
 \tag{1}$$

Where,  $\Delta = \sum_{j=a,b,c} (v_{1j1}^+)^2$   $\gamma = \tan(\phi) / \sqrt{3}$

This gives balanced source currents after compensation. The reference voltage for series inverter is

$$\begin{aligned}
 v_{inji}^* &= v_{li}^* - v_{ti} \\
 i &= a, b, c
 \end{aligned}$$

$v_{li}^*$  = desired load voltage and  $v_{inji}^*$  = reference series active filter voltage. The reference and actual quantities are given to the hysteresis current controller in order to get switching command for VSI switches. Whenever error limit exceeds a specified tolerance band  $\pm h$  then switching command is given to VSI switches [7]. The Control block diagram of UPQC is as shown in figure 3.



(a)

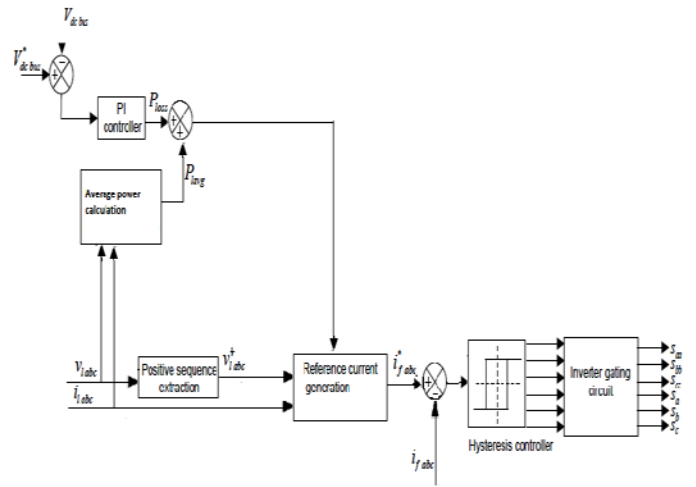


fig3. UPQC control part (a) series inverter control (b) shunt inverter control

#### 4 WIND ENERGY GENERATING SYSTEM

In this system, the wind generation is based on constant speed topology with pitch control turbine and induction generator is used in this system because of its simplicity as it does not require a separate field circuit and diode bridge rectifier is used to convert power generated by induction generator into dc power. The output power of the turbine is given by the following equation [8].

$$P_m = C_p(\lambda, \beta) \frac{\rho A}{2} (V_{wind})^3$$

where,

- $P_m$  Mechanical output power of the turbine (W)
- $C_p$  Performance coefficient of the turbine
- $\rho$  Air density (kg/m<sup>3</sup>)
- $A$  Turbine swept area (m<sup>2</sup>)
- $V_{wind}$  Wind speed (m/s)
- $\lambda$  Tip speed ratio of the rotor blade tip speed to wind speed
- $\beta$  Blade pitch angle (deg)

#### 5 MATLAB/SIMULINK MODEL

The power circuit is modeled as a three phase four wire system with a nonlinear load that is composed of a three phase diode bridge rectifier with RL load as shown in figure 4.

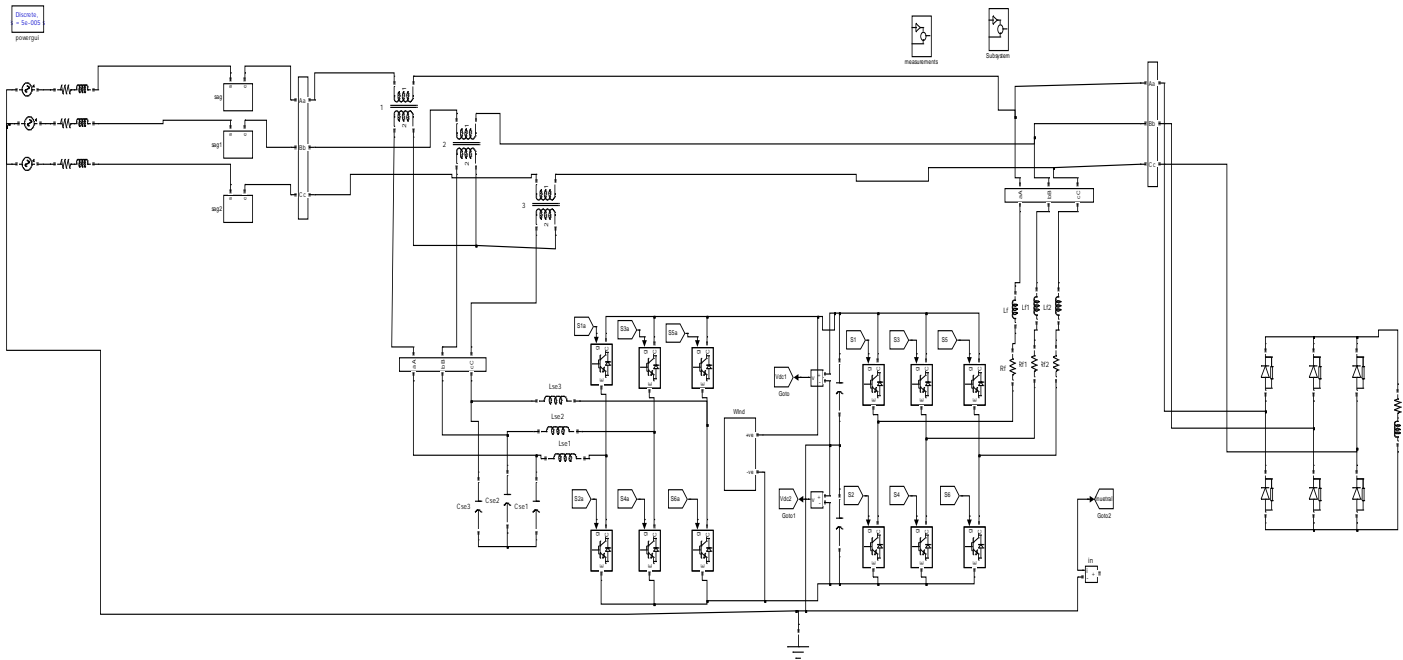


fig4. MATLAB/SIMULINK model of system

## 6 SIMULATION RESULTS

In this paper, three phase four wire 230V (line-neutral) 50Hz system is considered. There are two operation modes in the proposed system. One is called the interconnected mode, in which the DG provides power to the source and the load. The other is called the islanding mode, in which the DG provides power to the load only within its power rating. The operation of proposed system was verified through MATLAB/SIMULINK software.

Fig.5 shows the waveforms of source current, shunt invert-

er current and load current respectively. When a non-linear load injects harmonic current then it can be compensated using shunt inverter current of UPQC to make source current sinusoidal.

Fig.6 shows the Fast Fourier Transform (FFT) analysis of load current and source current. As shown in FFT analysis, the Total Harmonic Distortion (THD) of supply current is 0.69% and that of load current is 28%.

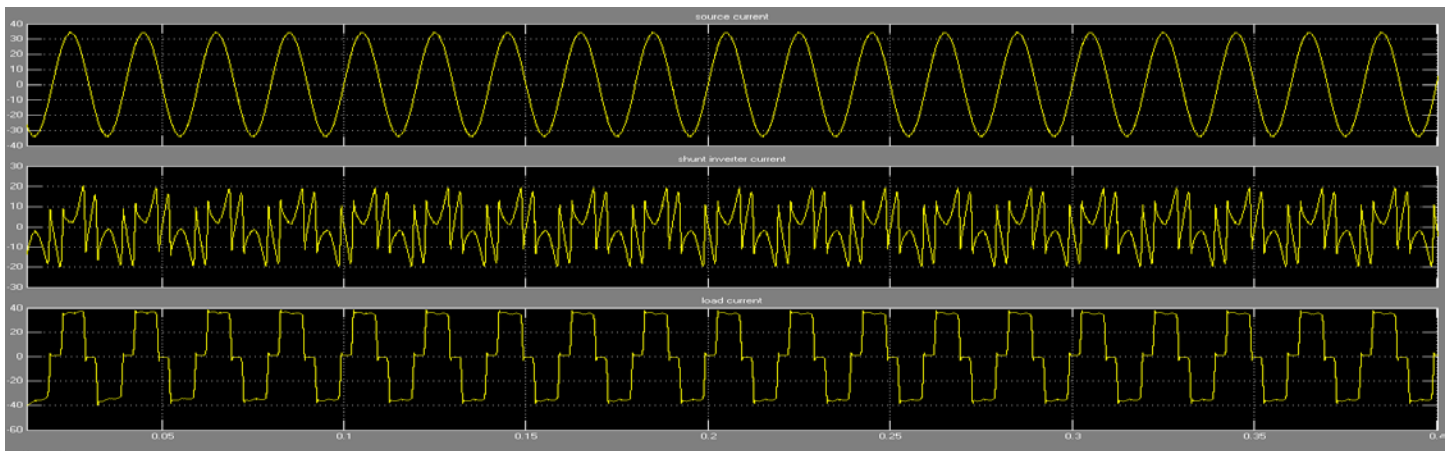


fig5. Current harmonic compensation (a) Source current (b) Shunt inverter current (c) Load current

(a)

(b)

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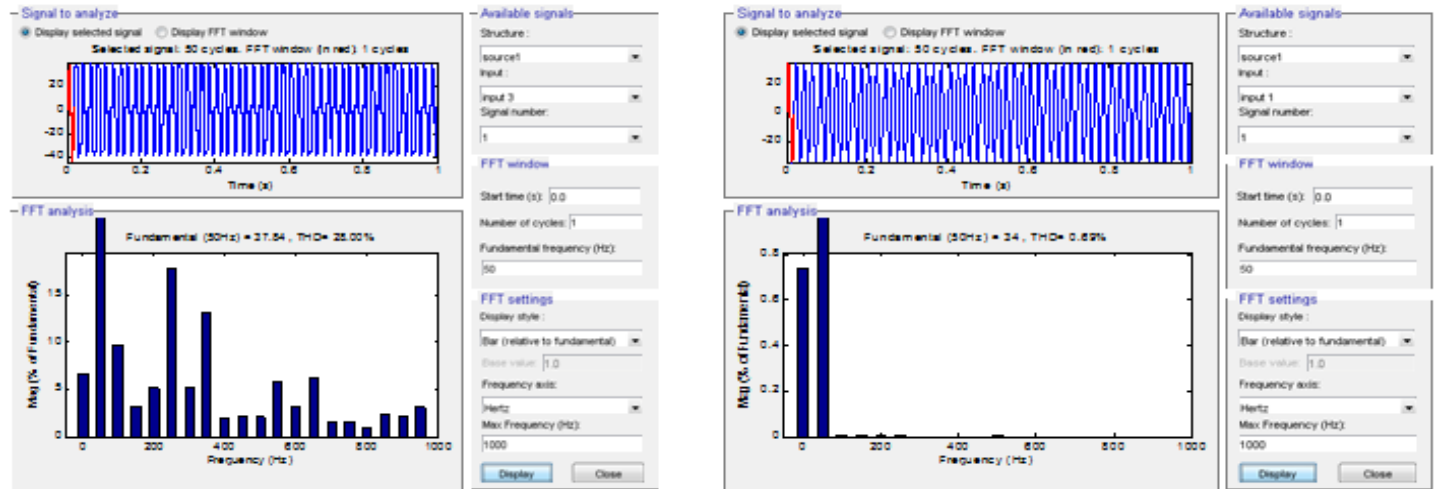


fig6. Fast Fourier Transform (FFT) analysis of (a) Load current (b) Source current

Fig. 7 represents waveforms of source voltage, series inverter voltage and load voltage. When unbalanced voltage sag (phase A has 10% of swell and phase B and C has 30% of sag) occurs in system from 0.2s to 0.6s then series inverter inject voltage to maintain load voltage at constant level.

Fig. 8 shows active power variation of load, shunt inverter, source and series inverter. During sag interval (from 0.2s to 0.6s) active power of source is reduced from 10 kW to 8kW then series inverter provides 2kW active power to cover this voltage sag.

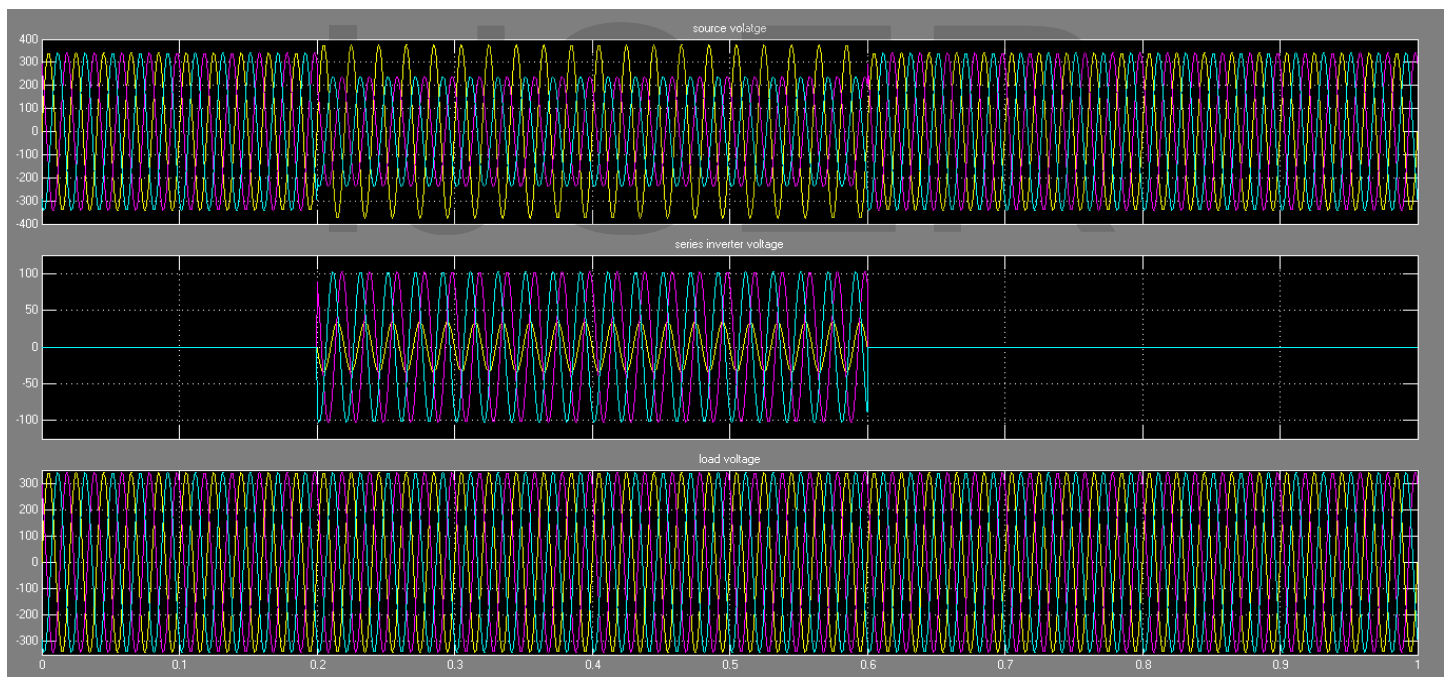


fig7. Voltage sag compensation (unbalanced voltage sag) (a) Source voltage (b) Series inverter voltage (c) Load voltage



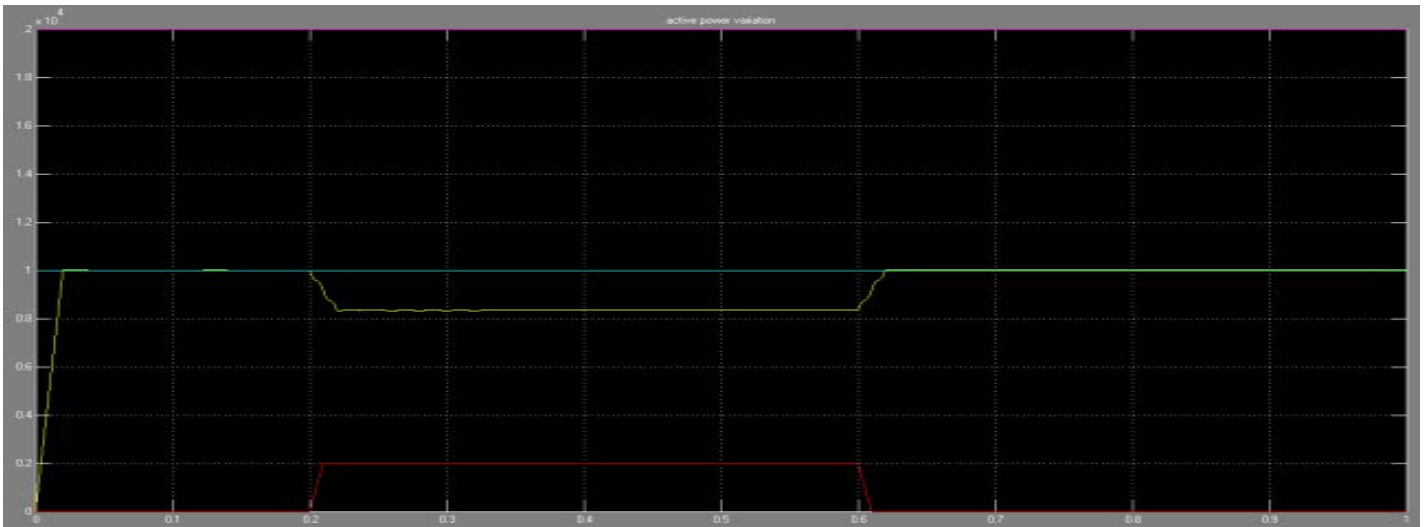


fig8. Active power of (a) Load (b) Shunt inverter (c) Source (d) Series inverter

Fig.9 shows waveforms of source voltage, shunt inverter voltage and load voltage. When voltage interruption occurs from 0.2s to 0.6s then during that interval shunt inverter inject voltage to maintain load voltage constant.

Fig.10 shows the active power of load, shunt inverter, source and series inverter.

In forward flow mode, shunt inverter with DG supplies power to the load in parallel with the main source. During normal operation, source and shunt inverter provides 10kW power to load respectively. But when voltage interruption occurs (from 0.2s to 0.6s) active power of source becomes zero and during this interval only shunt inverter provides 20kW active power to load.

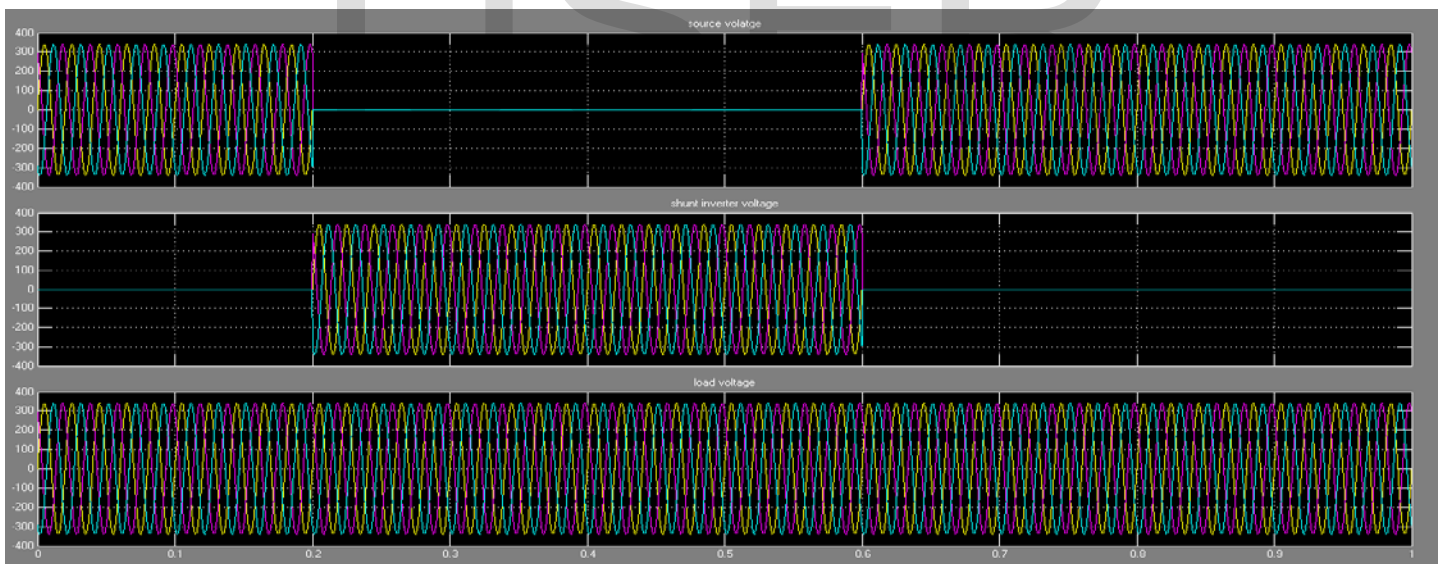


fig9. Voltage interruption (forward flow mode) (a) Source voltage (b) shunt inverter voltage (c) load voltage

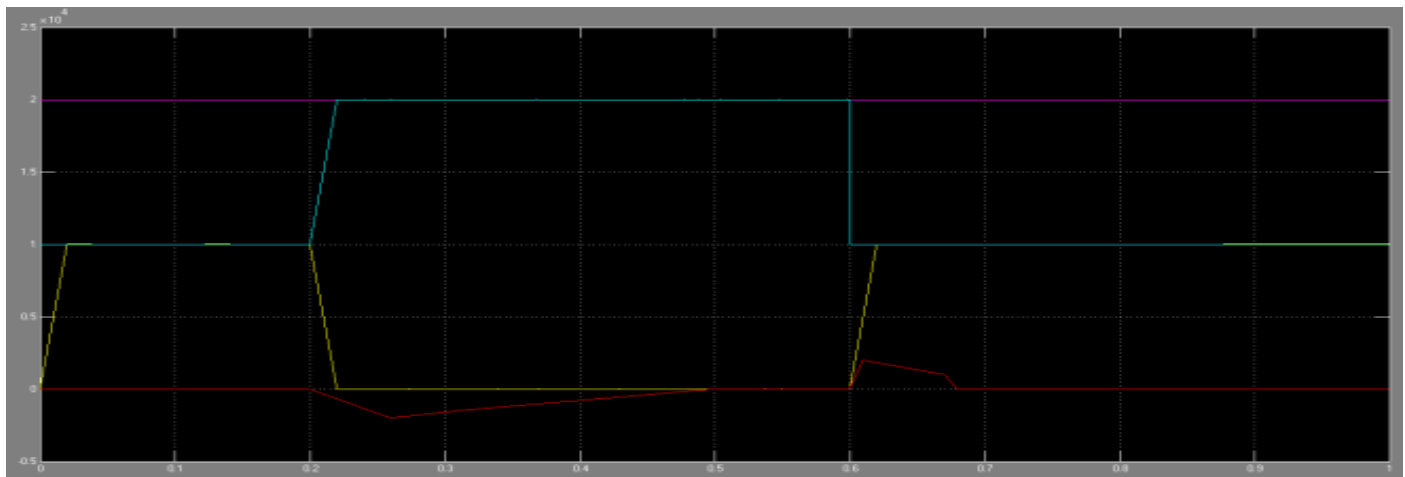


fig10. Active power of (a) load (b) shunt inverter (c) source (d) series inverter

Fig.11 represents source voltage, series inverter voltage, load voltage waveforms. The balanced voltage sag occur (all phases has 30% of sag) from 0.2s to 0.6s. During this time in-

terval series inverter inject voltage to cover this voltage sag and to maintain load voltage constant.

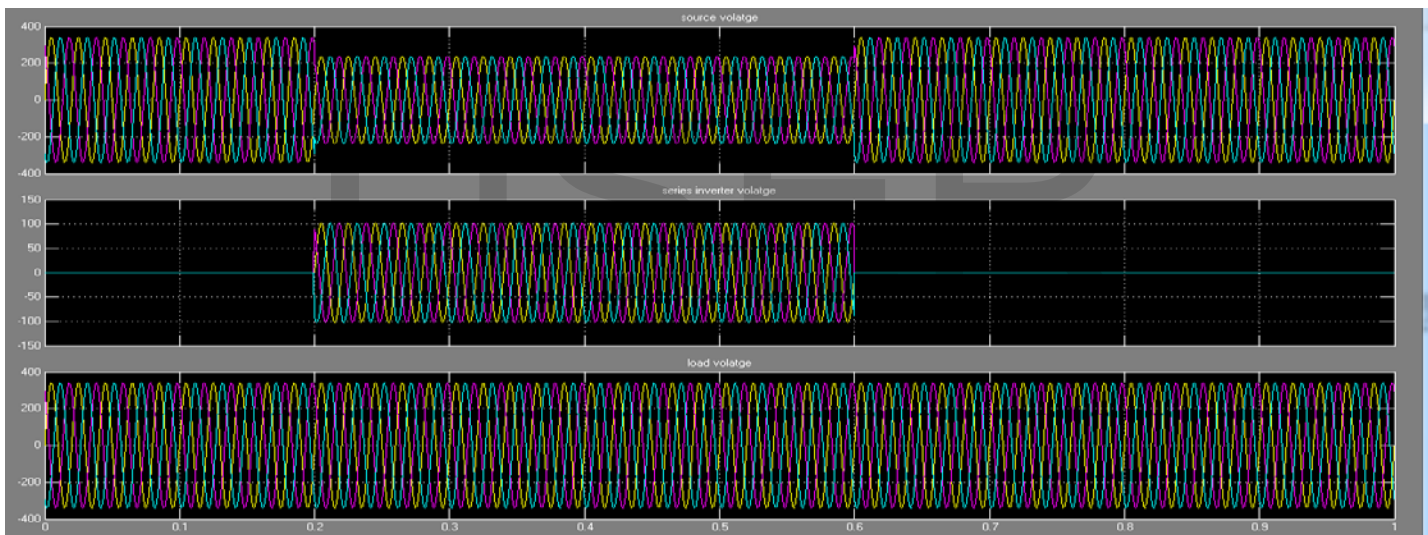


fig11. Balanced voltage sag compensation. (a) Source voltage (b) Series inverter voltage (c) Load voltage

Fig. 12 shows source voltage, shunt inverter voltage, and load voltage waveforms. When voltage interruption occurs from 0.2s to 0.6s then during that interval shunt inverter inject voltage to maintain load voltage constant. Fig.13 shows active power variation of shunt inverter, load, series inverter and source, respectively.

In reverse-flow mode, the shunt inverter with DG supplies power to the load and the main source. In normal operation, the shunt inverter provides 10-kW power to the load and the source, respectively. But during the voltage interruption, only the shunt inverter provides 10-kW power to the load.

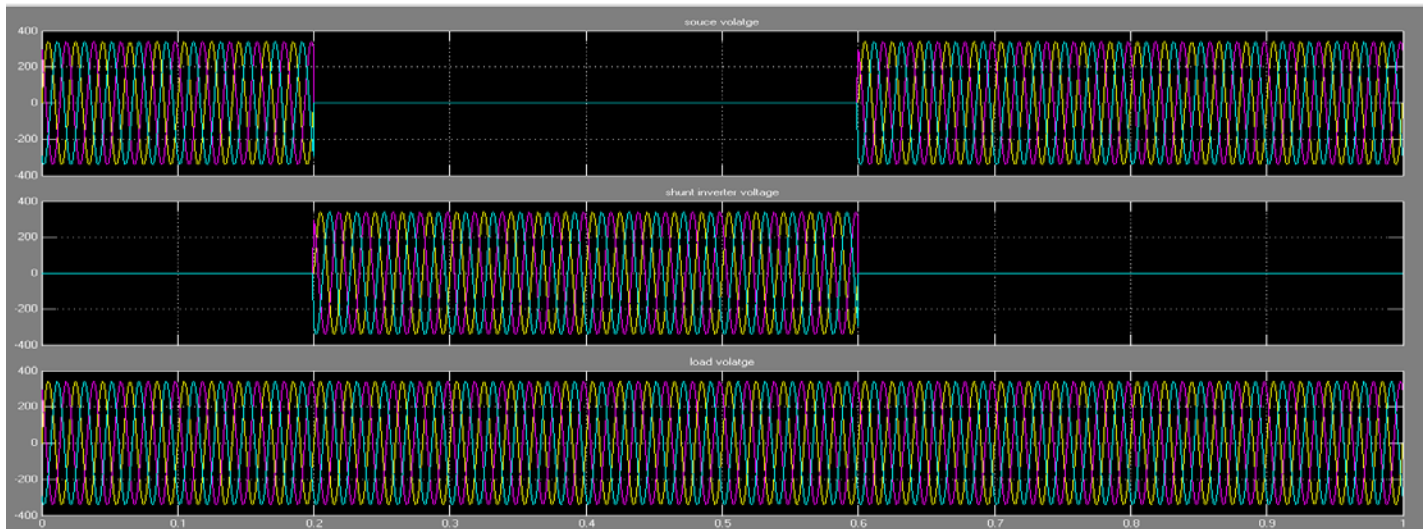


fig12. Voltage interruption (reverse flow mode) (a) Source voltage (b) Shunt inverter voltage (c) Load voltage

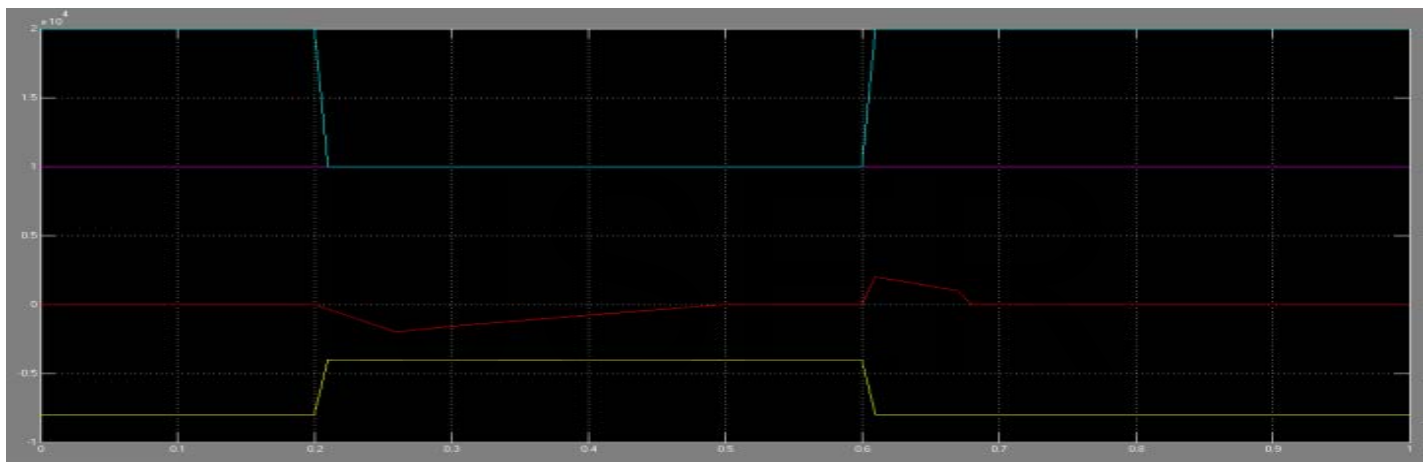


fig13. Variation of active power of (a) Shunt inverter (b) Load (c) Series inverter (d) Source

## 7 CONCLUSION

In this paper, the combined operation of UPQC with DG is explained. The proposed system is composed of series and shunt inverter, wind energy system connected to the DC link through rectifier. The proposed system is able to compensate voltage sag, voltage swell, voltage interruption and current harmonics in interconnected and islanding mode.

Hence, the proposed system improves power quality at the point of installation on power distribution system or industrial power systems. The operation of UPQC with DG has been evaluated through simulation studies using MATLAB/SIMULINK software.

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